

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

(Case No. P043D2C1)

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In the Application of:

FARMWALD ET AL.

Serial No: Continuation of 08/798,520

Filed: NOVEMBER 20, 1998

Title: INTEGRATED CIRCUIT I/O USING A
HIGH PERFORMANCE BUS INTERFACE

Group

Art Unit:

Before

Examiner:

Assistant Commissioner for Patents
Washington, DC 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the examination of the above-referenced application,
kindly amend the application as follows:

IN THE TITLE:

Please delete the title and substitute --SYNCHRONOUS MEMORY
DEVICE HAVING A PROGRAMMABLE REGISTER AND METHOD OF CONTROLLING
SAME--.

IN THE SPECIFICATION:

On page 3, line 9, delete "micro-processor" and substitute
--microprocessor--.

On page 6, line 1, delete "4,646,279" and substitute
--4,646,270--.